

中国科学院国家天文台

National Astronomical Observatories, CAS



ZENTRUM FÜR ASTRONOMIE

Univ. Heidelberg

UNIVERSITÄT HEIDELBERG

Zukunft. Seit 1386.



# Introduction to GPU Accelerated Computing

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VolkswagenStiftung

the SILK ROAD PROJECT at NAOC/KIAA

丝绸之路计划

Picture: Xishuangbanna, Yunnan, China by R.Sp.



北京大学 PEKING UNIVERSITY



# Introduction to GPU Accelerated Computing

Feb. 20-24, 2023

## Table of Contents (subject to adjustment/change):

1. Monday morning: General Introduction Computer Architecture, Many-Core, GPU and others..., Access...
2. Monday afternoon: Access to kepler, CUDA Hello, GPU Properties, Simple Add, Vector Add
3. Tuesday morning: More on GPU Software and Hardware
4. Tuesday afternoon: CUDA More Vector Add, Scalar Products, Using Blocks and Threads
5. Wednesday morning: Parallelization and Amdahl's Law, GPU Acceleration, Future Architecture
6. Wednesday Afternoon: Events, Histograms, Matrix Multiplication
7. Thursday Morning: Astrophysical N-Body Code
8. Thursday Afternoon: Astrophysical Parallel N-Body Code Using MPI and GPU
9. Friday Morning: Continue Histograms, Wrap-Up, Q+A, Other Lectures (Wen-Mei Hwu)



Observations (Experiment)



Theory

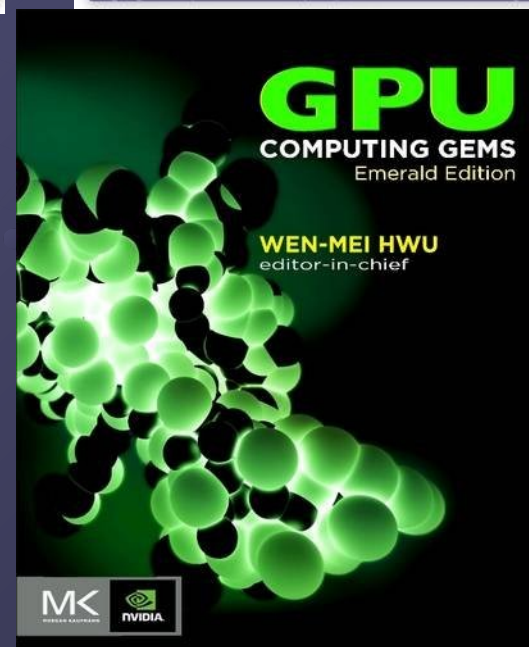
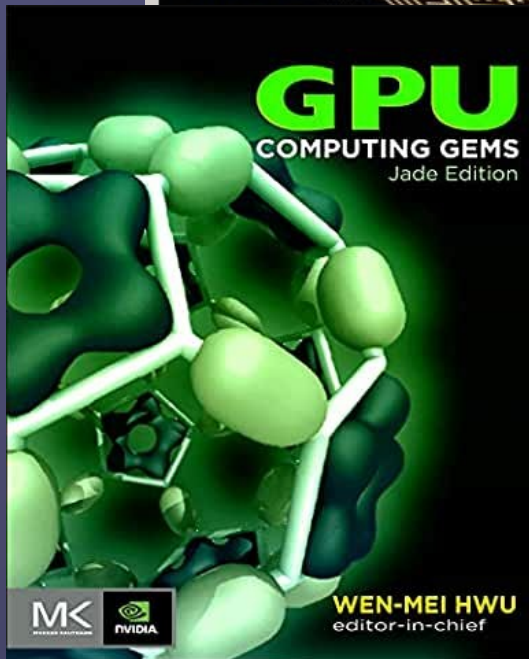
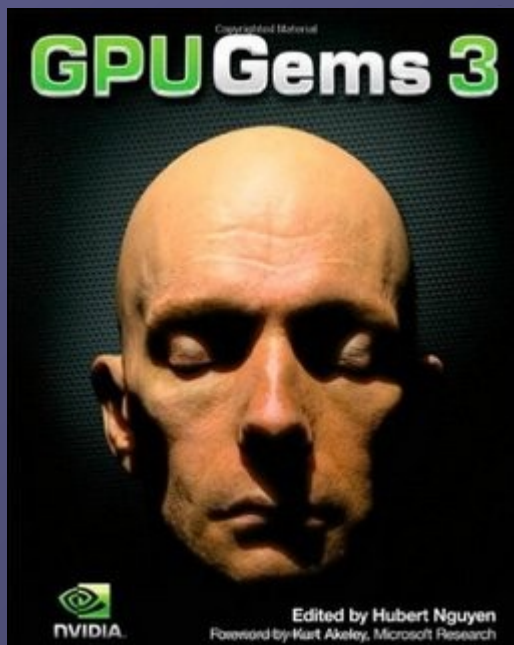
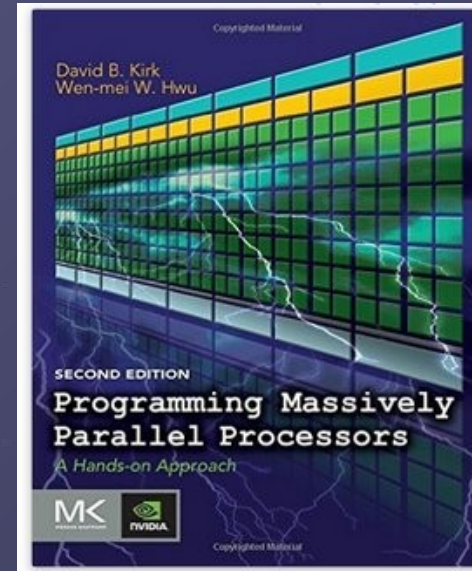
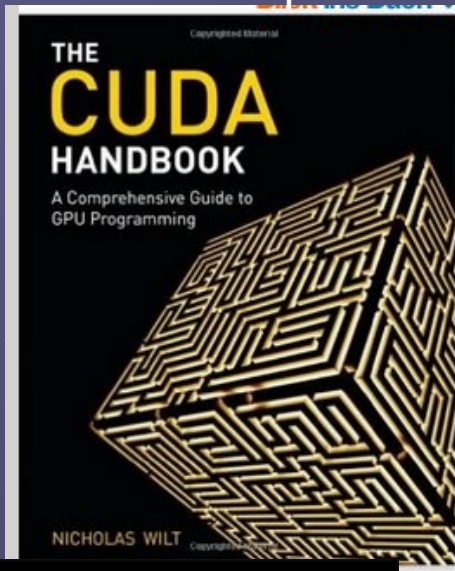
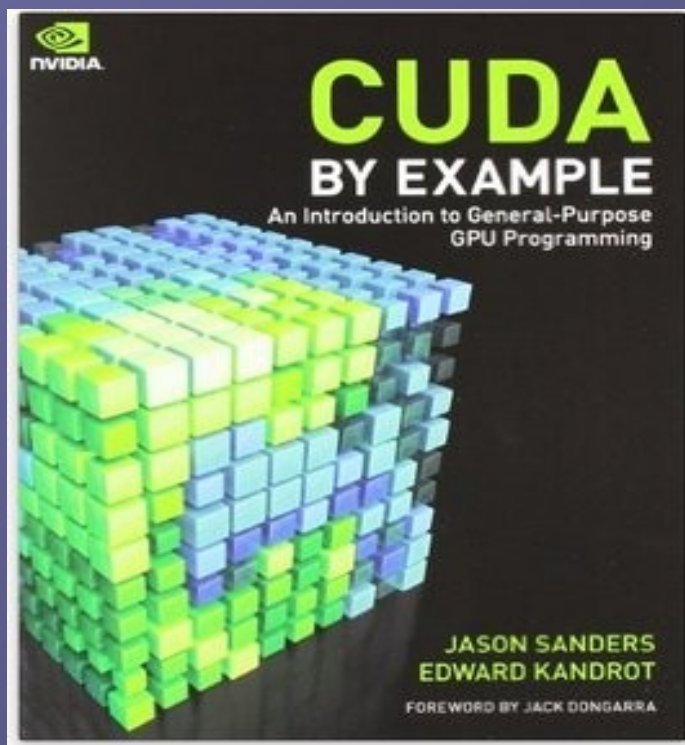


Computational Physics





Literature: why NVIDIA? CUDA ... ?  
easy to learn!! runs on our training system kepler  
future? SYCL/openCL? HIP / HIPIFY ?



# GPU Computing

# History

# History



**Erik Holmberg (1908-2000)**

Dissertation Univ. Lund (Schweden) (1937):

“A study of double and multiple galaxies”

Galaxies often in Groups and Pairs

Irregular Distribution of Satellite Galaxies

(Holmberg-Effect)

**Father of numerical astrophysics?**

» **...with 200 light bulbs**

# History

<http://cdsads.u-strasbg.fr/abs/1941ApJ....94..385H>

## The Astrophysical Journal, Nov. 1941



**LUMA METALL**

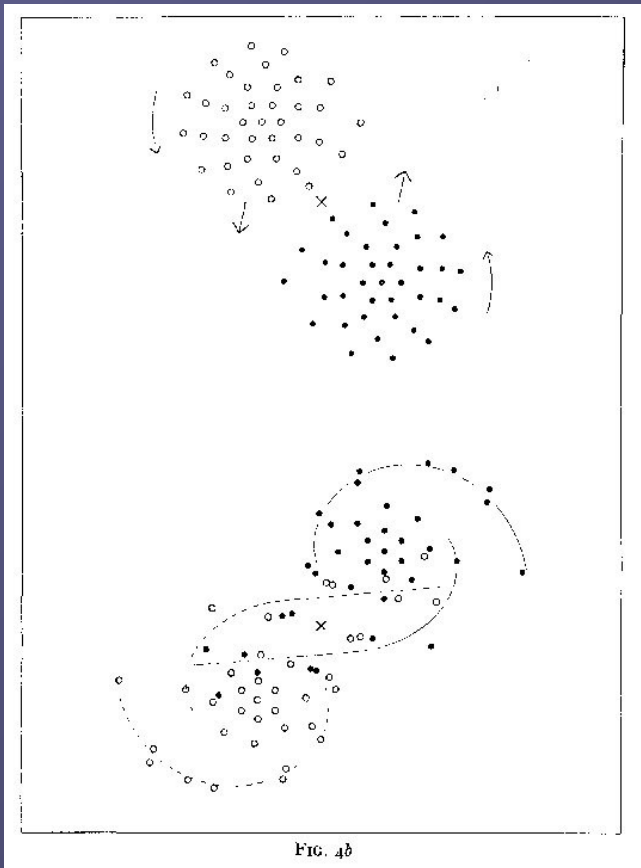


FIG. 4b

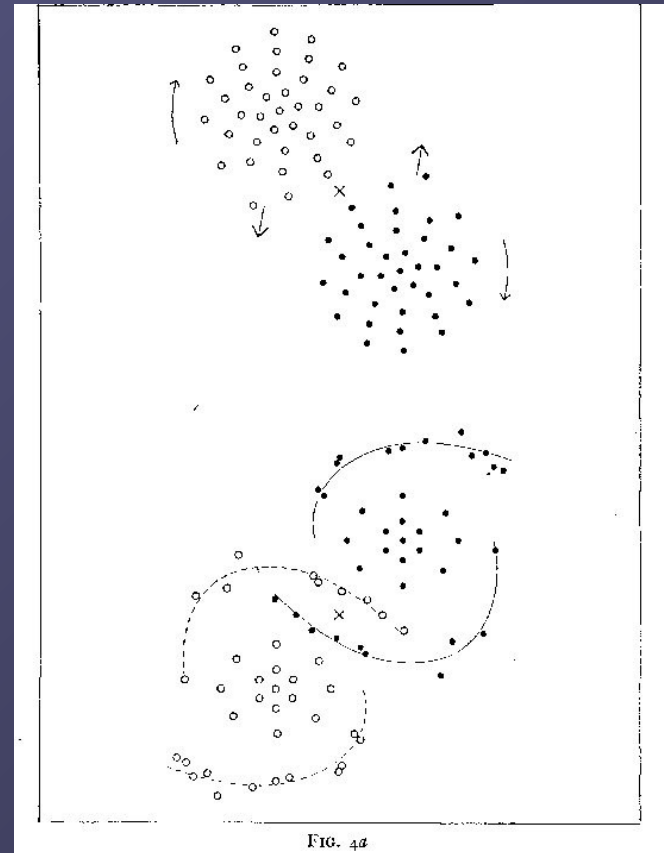


FIG. 4a



# HARDWARE

...before von Neumann...

● Konrad Zuse (1910-1995) Berlin



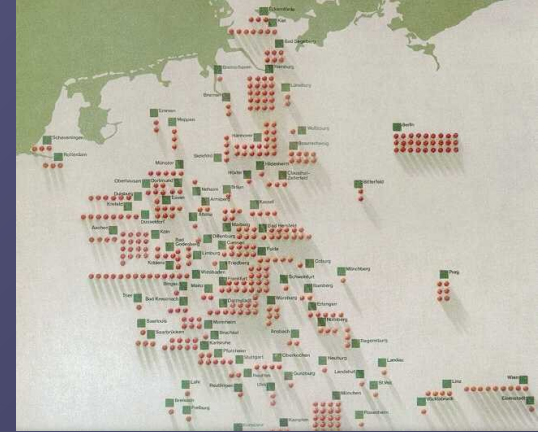
Invented freely programmable Computer



Z1 in parental flat 1936



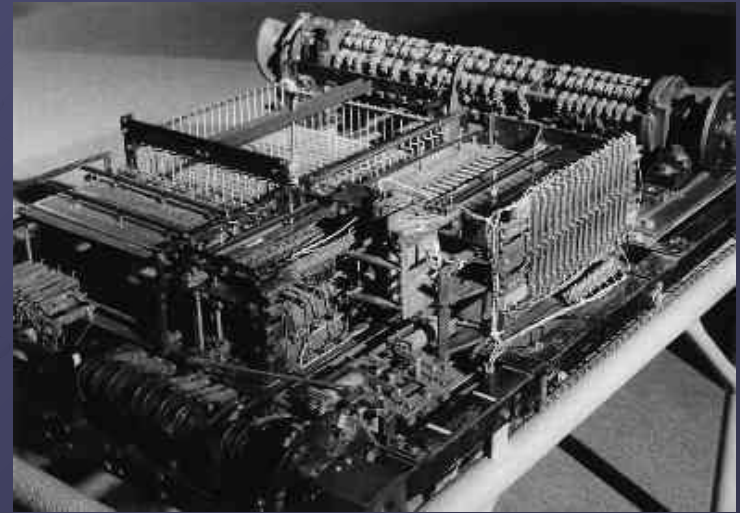
# History



**Zuse Z4: 1944 Berlin, 1950 Zürich, 1954 Frankreich  
1959 Deutsches Museum München**



**Computing Speed 0.03 MHz**



**Memory 256 byte**

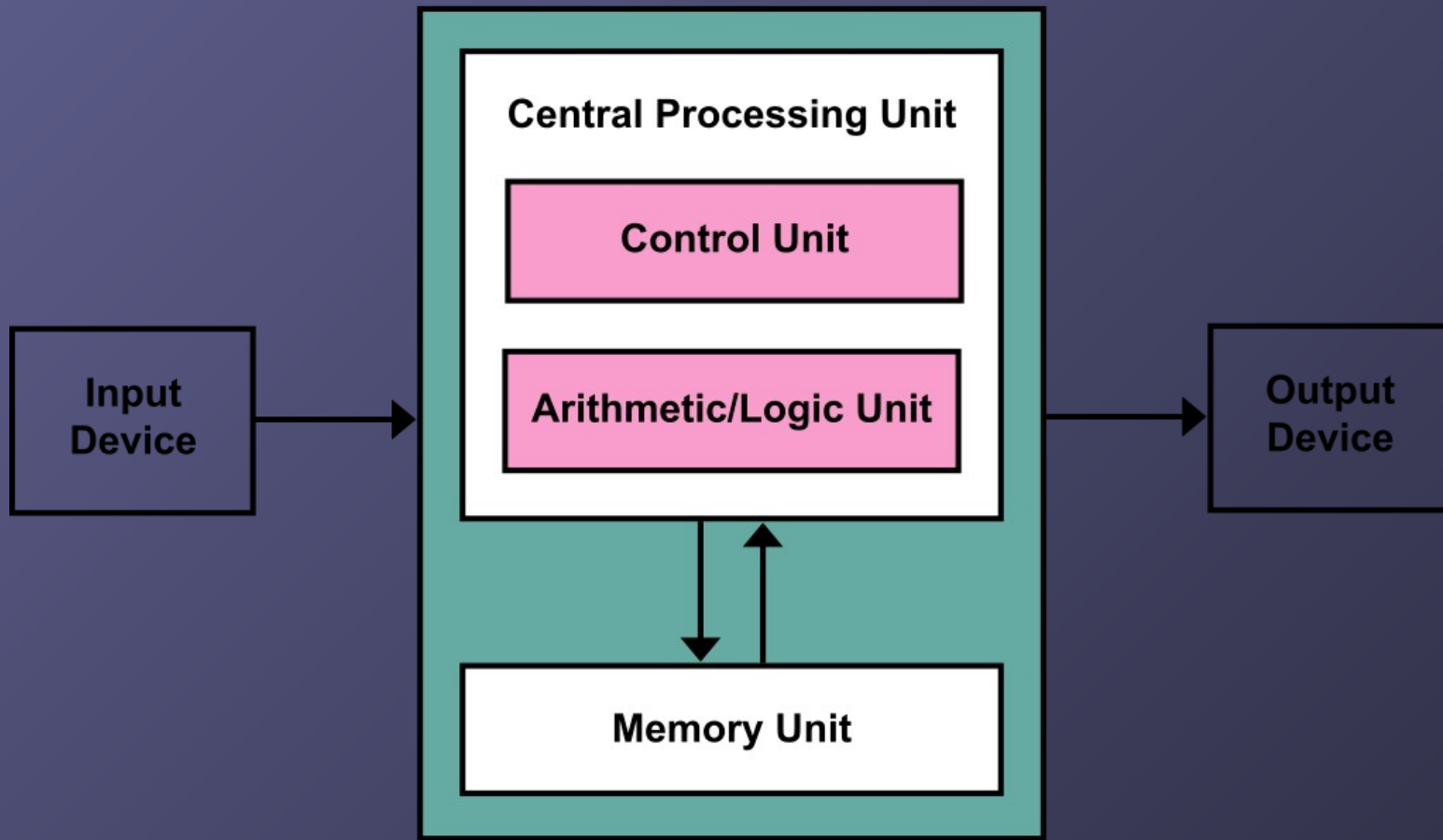
# HARDWARE

- John von Neumann (1903-1957)

Born Budapest, Lecturer Berlin, since 1930 Princeton Univ.

Fundamental Architecture of an electronic computing device(1946)

Source: [https://en.wikipedia.org/wiki/Von\\_Neumann\\_architecture#/media/File:Von\\_Neumann\\_Architecture.svg](https://en.wikipedia.org/wiki/Von_Neumann_architecture#/media/File:Von_Neumann_Architecture.svg)





Astronomisches  
Rechen-Institut (ARI)  
at Univ. of  
Heidelberg, Germany



**Siemens 2002  
Computer in 1964  
At ARI**



# History

<http://cdsads.u-strasbg.fr/abs/1960ZA.....50..184V>

Astronomisches Rechen-Institut in Heidelberg  
Mitteilungen Serie A Nr. 14

## Die numerische Integration des $n$ -Körper-Problemes für Sternhaufen I

Von

SEBASTIAN VON HOERNER

Mit 3 Textabbildungen

(Eingegangen am 10. Mai 1960)

Astronomisches Rechen-Institut in Heidelberg  
Mitteilungen Serie A Nr. 19

## Die numerische Integration des $n$ -Körper-Problems für Sternhaufen, II.

Von

SEBASTIAN VON HOERNER

Mit 10 Textabbildungen

(Eingegangen am 19. November 1962)

<http://cdsads.u-strasbg.fr/abs/1963ZA.....57...47V>

Tabelle 5. Zahl der gegenseitigen Umläufe, Häufigkeit des Auftretens und kleinster gegenseitiger Abstand  $D_m$  der engsten Paare. (Alle engsten Paare mit mehr als zwei vollen Umläufen wurden notiert)

Umläufe	Häufigkeit	$D_m$
2—3	11	0.0102
3—5	9	0.0177
5—10	5	0.0070
10—20	2	0,0141
20—50	1	0.0007
50—100	1	0.0035
100—200	1	0.0039

S.v. Hoerner,  
Z.f.Astroph. 1960, 63

Siemens 2002  
N=4,8,12,16 (4 Trx)

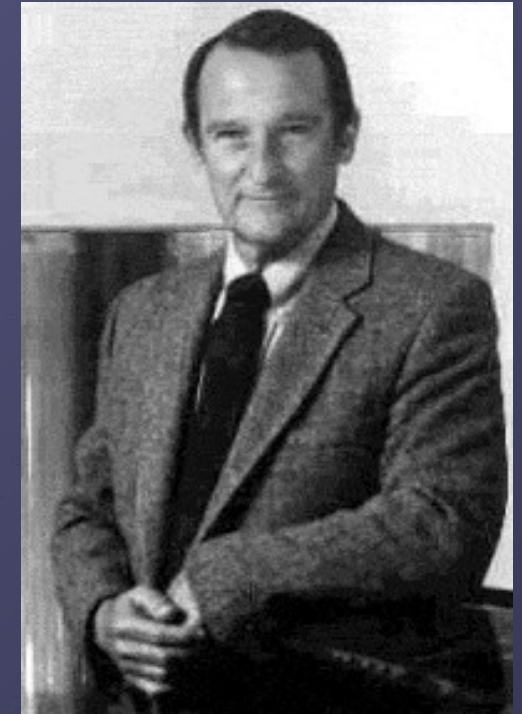
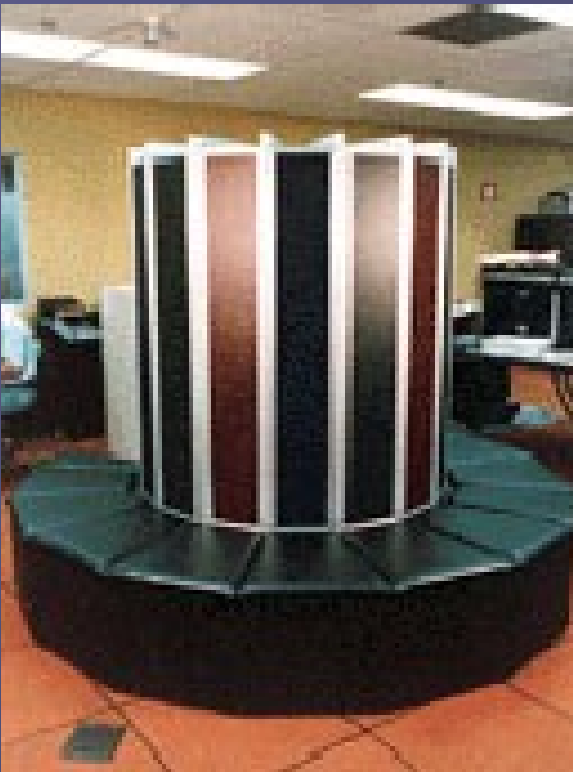
N=16,25 (40 Trx)

# History

## ● Seymour Cray (1925-1996)

“father of supercomputing”

[https://en.wikipedia.org/wiki/Women\\_in\\_computing](https://en.wikipedia.org/wiki/Women_in_computing)



**CRAY1: Vectorregisters (1976)**  
**160 Mflop, 80 MHz, 8 MByte RAM**

**CRAY2: (1984)**  
**1Gflop, 120MHz, 2GByte RAM**

# History

*Supercomputer  
JUGENE  
IBM Blue Gene  
At FZ Jülich,  
Germany*



*Opening Ceremony June 2008*



# Computational Science...

...after von Neumann...

Exaflop/s?

Petaflop/s

Teraflop/s

Gigaflop/s

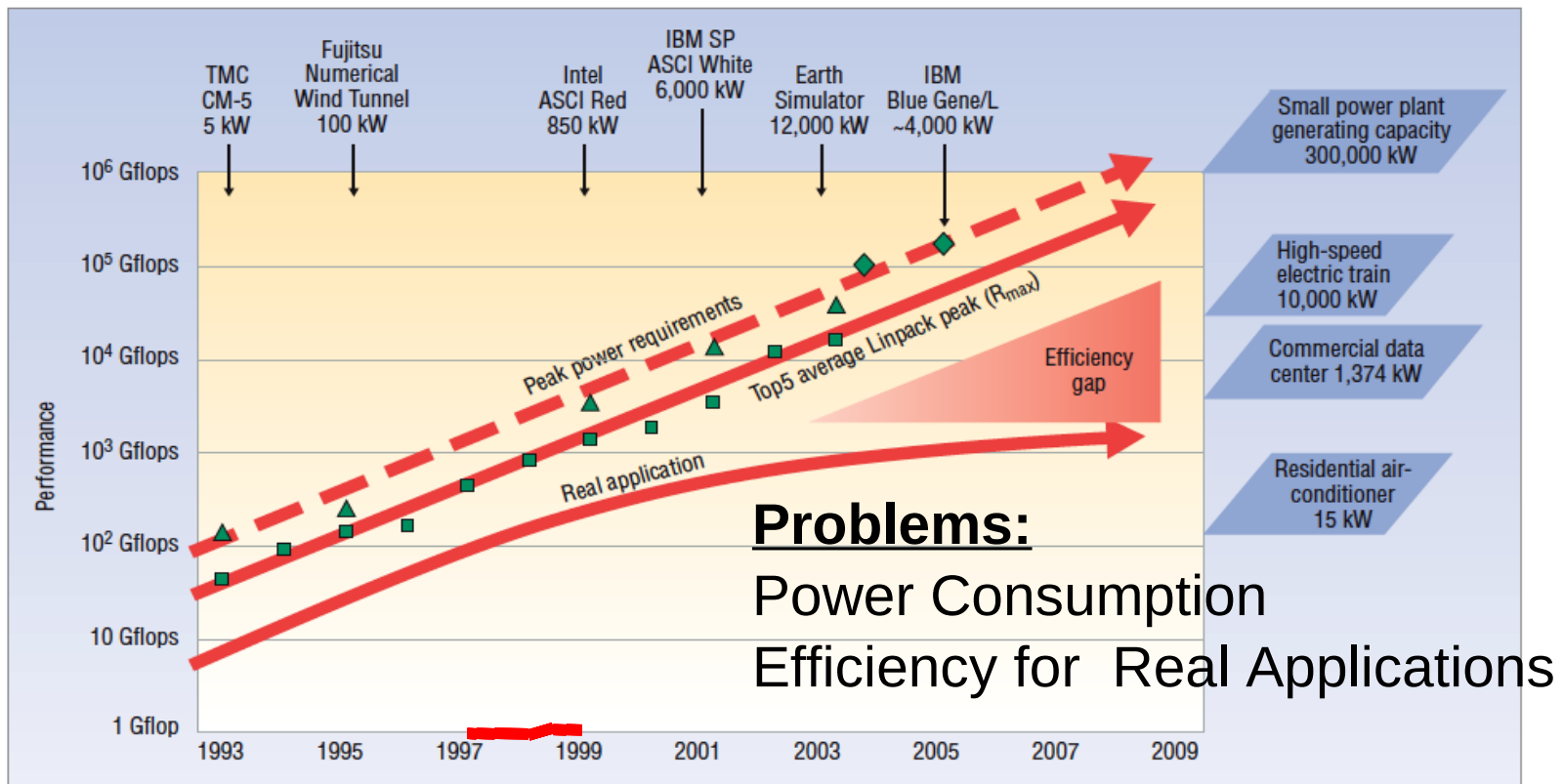


Figure 1. Rising power requirements. Peak power consumption of the top supercomputers has steadily increased over the past 15 years.

Thanks to Horst Simon, LBNL/NERSC for this diagram.

GPU Computing

# Special Hardware Accelerators

# SPECIAL HARDWARE

## CPUs

Central Processing Units



General Purpose oriented

1-12 Cores

Up to 4 pipes per core using Vector Units

Fully Programmable, many languages available

Very well studied

Max. 125W per processor

## GPUs

Graphic Processing Units



Graphics oriented

16-512 Cores

Massively Parallel Architecture, specialized instructions for parallel processing

Fully programmable, but limited languages

Algorithms not fully explored

Max. 400W per card

## FPGAs

Field Programmable Gate Arrays



Custom designs, best for processing streaming data

Programmable Logic, Architecture is custom-built for the required application

Requires extensive knowledge to program, development time is longer than CPUs and GPUs

Application interface is custom built on each case

Max. 60W per FPGA

## ASICs

Application Specific Integrated Circuits



Fully custom designs, built for a specific application

Not flexible, cannot be changed once it is built

Development is even more specialized than FPGAs

Power consumption varies with the application, usually best performance per Watt

Slide: Guillermo Marcus

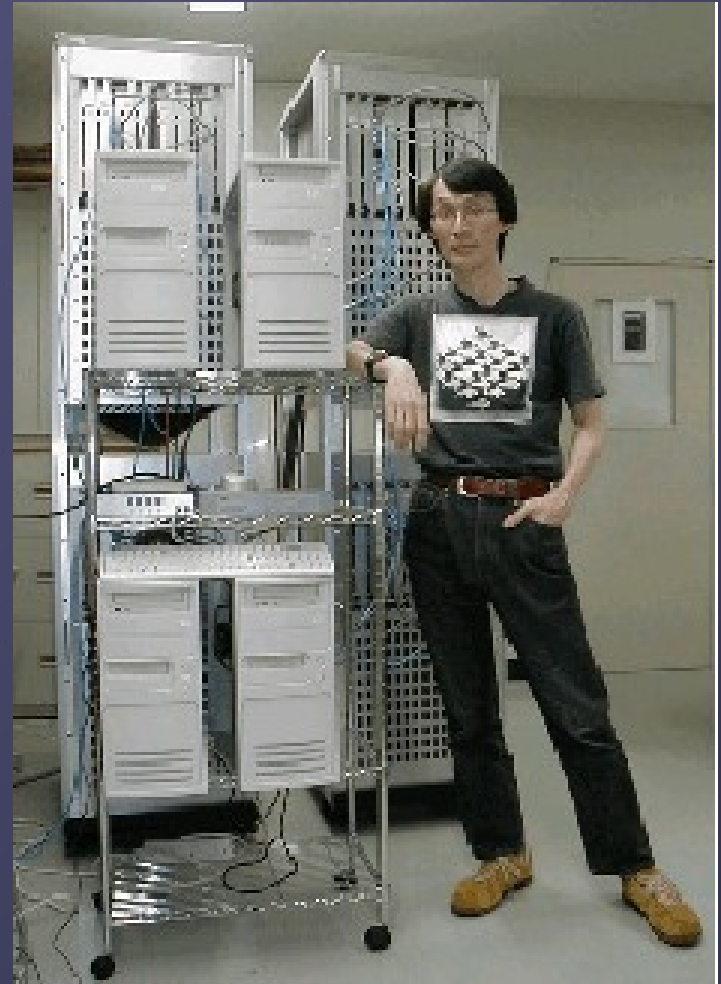




# HARDWARE

## GRAPE-6 Gravity/Coulomb Part

- G6 Chip:  $0.25\mu$  2MGate ASIC, 6 Pipelines
- at 90MHz, 31Gflops/chip
- 48Tflops full system (March 2002)
- Plan up to 72Tflops full system (in 2002)
- Installed in Cambridge, Marseille, Drexel, Amsterdam, New York (AMNH), Mitaka (NAO), Tokyo, etc..  
New Jersey, Indiana, Heidelberg



## GRAPE-6



1998, 120  
Gflops

Developers: Junichiro Makino, Toshiyuki Fukushige, Hiroshi Daisaka, Eiichiro Kokubo, Masaki Koga, Makoto Taiji, Ken Namura

[GRAPE-6: Massively-Parallel Special-Purpose Computer for Astrophysical Particle Simulations](#)

[Sales information](#)

## The Green500 List - November 2010

Listed below are the November 2010 The Green500's energy-efficient supercomputers ranked from 1 to 100.

<http://www.green500.org>

Green500 Rank	MFLOPS/W	Site*	Computer*	Total Power (kW)
1	1664.20	IBM Thomas J. Watson Research Center	NNSA/SC Blue Gene/Q Prototype	38.80
2+	1448.03	National Astronomical Observatory of Japan	GRAPE-DR accelerator Cluster, Infiniband	24.59
2	958.35	GSIC Center, Tokyo Institute of Technology	HP ProLiant SL390s G7 Xeon 6C X5670, Nvidia GPU, Linux/Windows	1243.80
3	933.06	NCSA	Hybrid Cluster Core i3 2.93Ghz Dual Core, NVIDIA C2050, Infiniband	36.00

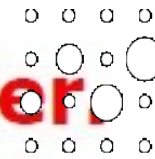
# GPU: NAOC laohu cluster Beijing, China





Heidelberg

# Kepler GPU cluster



VolkswagenStiftung

## Kepler GPU cluster

12 nodes = 12 x 16 = 192 CPU cores (@ 2 GHz)

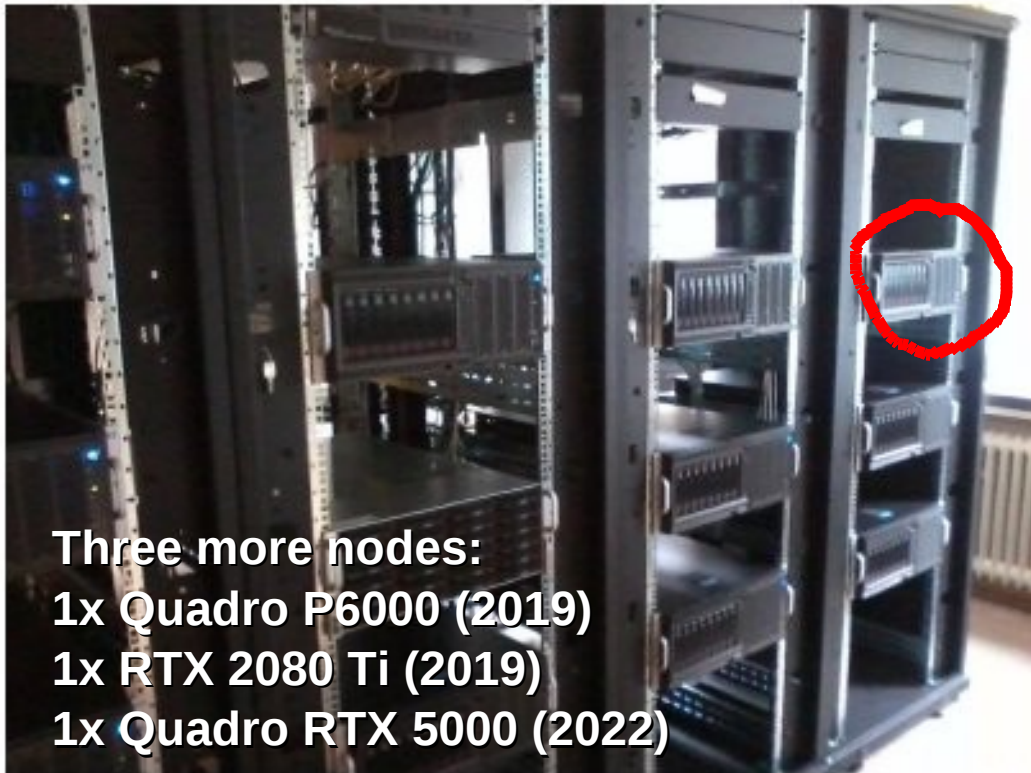
12 x 64 GB = 768 GB RAM CPU memory

12 GPUs K20m = 12 x 2496 ~ 30k GPU threads

12 x 4.8 GB ~ 57 GB GPU device memory

4 x Xilinx Virtex-6 FPGA (ML 605)

since beg. 2013 operated.



Three more nodes:

1x Quadro P6000 (2019)

1x RTX 2080 Ti (2019)

1x Quadro RTX 5000 (2022)

# NVIDIA Volta V100 GPU, 21 billion transistors, 5120 cores (now Ampere A100, 6920 cores, 9.7 Tflops DP, 19.5 SP) (Hopper H100, ...)



With NVLINK

Without NVLINK



## PERFORMANCE

with NVIDIA GPU Boost\*

DOUBLE-PRECISION

7.8<sub>teraFLOPS</sub>

DOUBLE-PRECISION

7<sub>teraFLOPS</sub>

SINGLE-PRECISION

15.7<sub>teraFLOPS</sub>

SINGLE-PRECISION

14<sub>teraFLOPS</sub>

DEEP LEARNING

125<sub>teraFLOPS</sub>

DEEP LEARNING

112<sub>teraFLOPS</sub>

## INTERCONNECT BANDWIDTH

Bi-Directional

NVLINK

300<sub>GB/s</sub>

PCIe

32<sub>GB/s</sub>

## MEMORY

CoWoS Stacked HBM2

CAPACITY

32/16<sub>GB HBM2</sub>

BANDWIDTH

900<sub>GB/s</sub>

## POWER

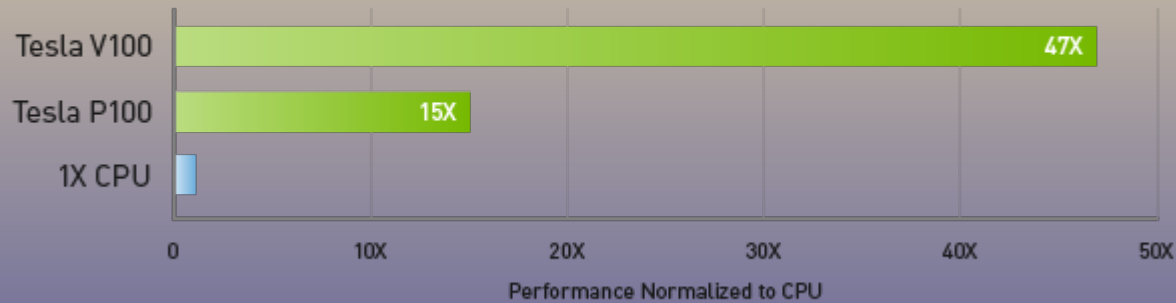
Max Consumption

300<sub>WATTS</sub>

250<sub>WATTS</sub>

# NVIDIA Volta V100 GPU, 21 billion transistors, 5120 cores

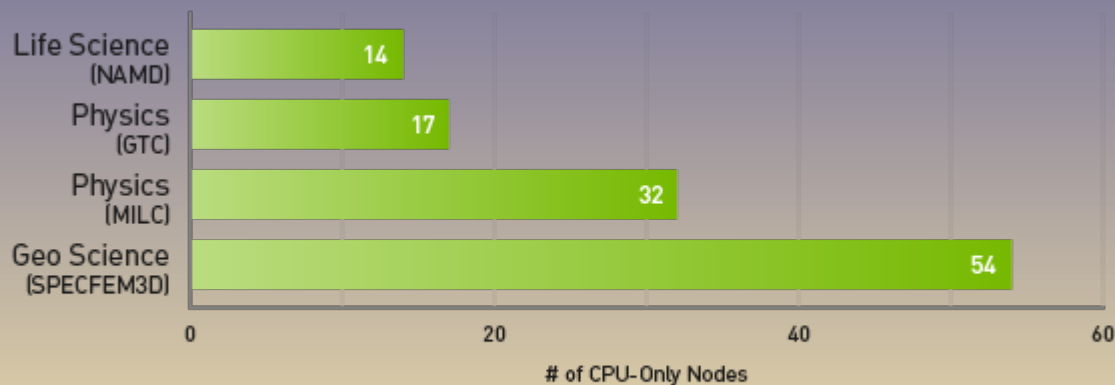
## 47X Higher Throughput Than CPU Server on Deep Learning Inference



Workload: ResNet-50 | CPU: 1X Xeon E5-2690v4 @ 2.6 GHz | GPU: Add 1X Tesla P100 or V100

## 1 GPU Node Replaces Up To 54 CPU Nodes

Node Replacement: HPC Mixed Workload



CPU Server: Dual Xeon Gold 6140@2.30GHz, GPU Servers: same CPU server w/ 4x V100 PCIe | CUDA Version: CUDA 9.x | Dataset: NAMD (STMV), GTC (mpi#proc.in), MILC (APEX Medium), SPECFEM3D (four\_material\_simple\_model) | To arrive at CPU node equivalence, we use measured benchmark with up to 8 CPU nodes. Then we use linear scaling to scale beyond 8 nodes.

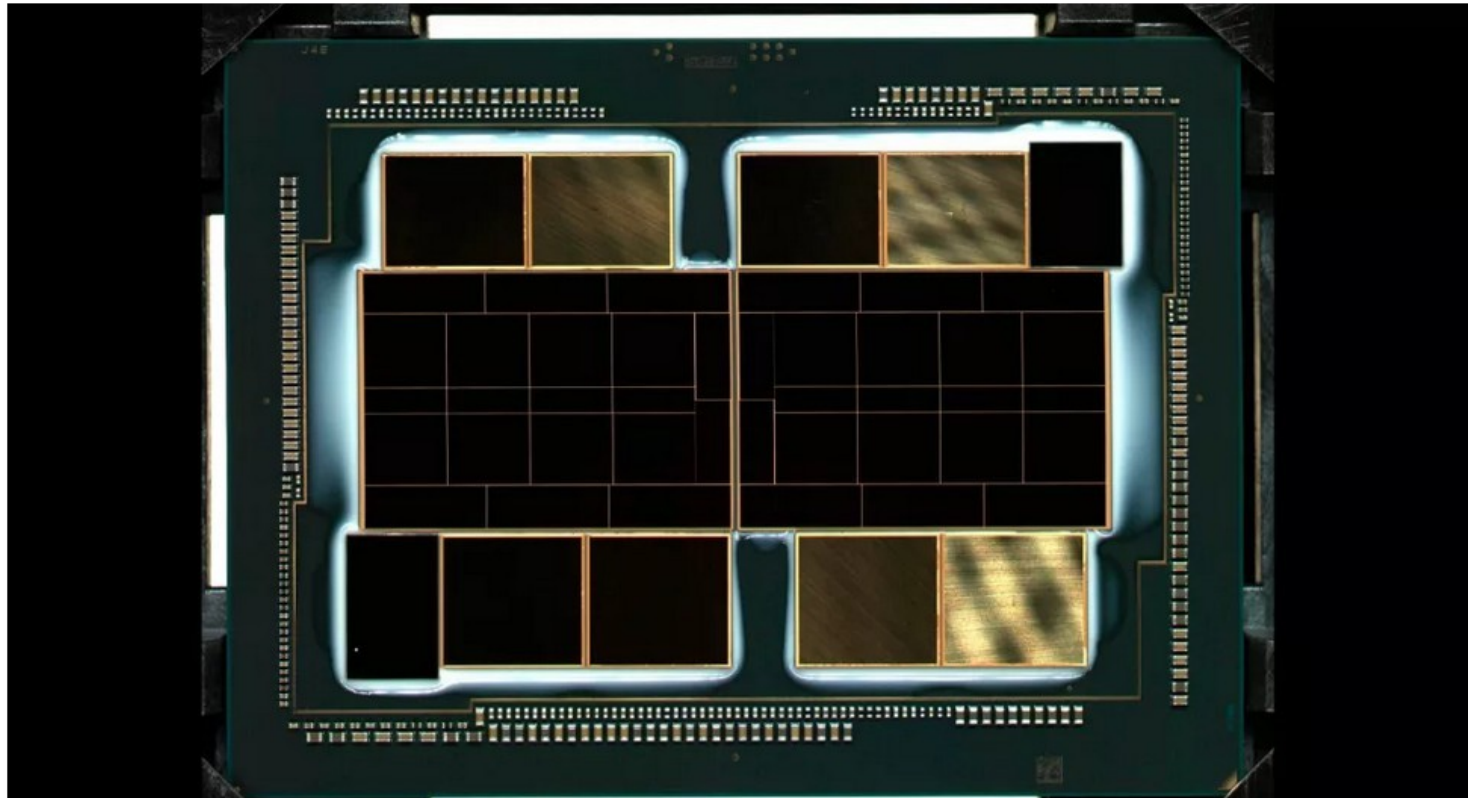


## New Intel Ponte Vecchio hardware / Recent Processors

### Intel Ponte Vecchio Seemingly Offers 2.5x Higher Performance Than Nvidia's A100

By Zhiye Liu published August 22, 2022

The battle of juggernauts



Intel has detailed the company's Ponte Vecchio Xe-HPC GPU at Hot Chips 34. In the provided benchmarks, the chipmaker claims that Ponte Vecchio delivers up to 2.5x more performance than the [Nvidia A100](#). But, as customary, take vendor-provided benchmarks with a pinch of salt.

From: <https://www.tomshardware.com/news/intel-ponte-vecchio-seemingly-offers-25x-higher-performance-than-nvidias-a100>

# AMD Instinct™ MI250X

## GPU Specifications

**GPU Architecture:** CDNA2

**Lithography:** TSMC 6nm FinFET

**Stream Processors:** 14,080

**Compute Units:** 220

**Peak Half Precision (FP16) Performance:**  
383 TFLOPs

**Peak Engine Clock:** 1700 MHz

**Peak Single Precision Matrix (FP32) Performance:**  
95.7 TFLOPs

**Peak Double Precision Matrix (FP64) Performance:**  
95.7 TFLOPs

**Peak Single Precision (FP32) Performance:**  
47.9 TFLOPs

**Peak Double Precision (FP64) Performance:**  
47.9 TFLOPs

**Peak INT4 Performance:** 383 TOPs

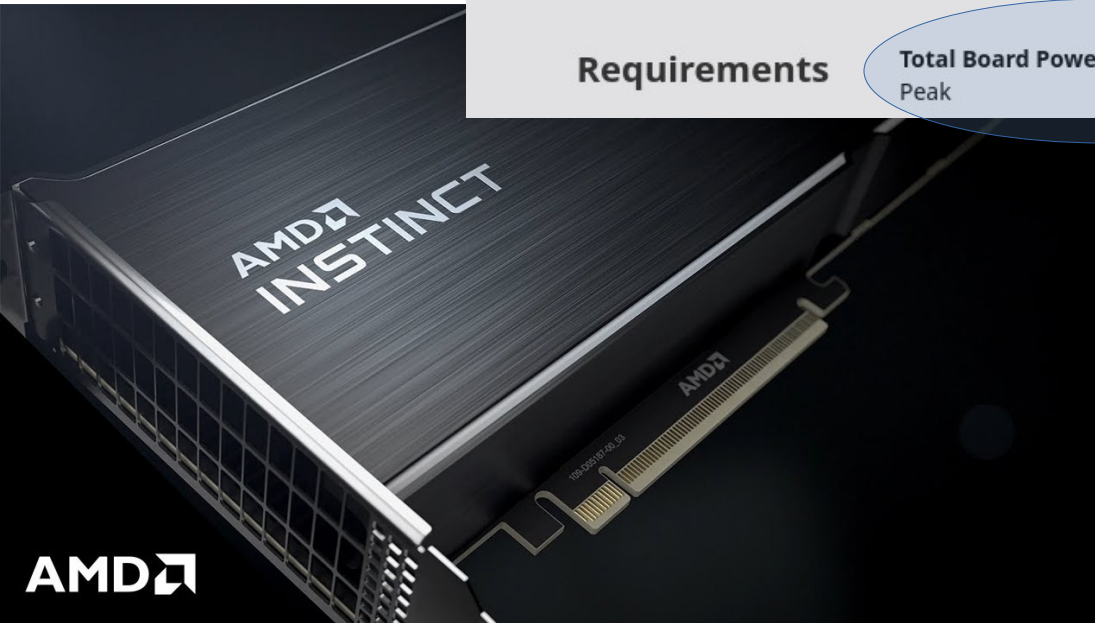
**Peak INT8 Performance:** 383 TOPs

**Peak bfloat16:** 383 TFLOPs

**OS Support:** Linux x86\_64

## Requirements

**Total Board Power (TBP):** 500W | 560W Peak

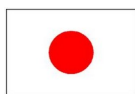




From <https://www.top500.org/>  
Nov. 2022 List



**USA**



**Japan**



**Finland  
(EUR)**



**Italy  
(EUR)**



**USA**

Rank	System	Cores	Rmax (PFlop/s)	Rpeak (PFlop/s)	Power (kW)
1	<b>Frontier</b> - HPE Cray EX235a, AMD Optimized 3rd Generation EPYC 64C 2GHz, AMD Instinct MI250X, Slingshot-11, HPE DOE/SC/Oak Ridge National Laboratory United States	8,730,112	1,102.00	1,685.65	21,100
		<b><u>GPU AMD Instinct</u></b>			
2	<b>Supercomputer Fugaku</b> - Supercomputer Fugaku, A64FX 48C 2.2GHz, Tofu interconnect D, Fujitsu RIKEN Center for Computational Science Japan	7,630,848	442.01	537.21	29,899
		<b><u>Fujitsu Arm</u></b>			
3	<b>LUMI</b> - HPE Cray EX235a, AMD Optimized 3rd Generation EPYC 64C 2GHz, AMD Instinct MI250X, Slingshot-11, HPE EuroHPC/CSC Finland	2,220,288	309.10	428.70	6,016
		<b><u>GPU AMD Instinct</u></b>			
4	<b>Leonardo</b> - BullSequana XH2000, Xeon Platinum 8358 32C 2.6GHz, NVIDIA A100 SXM4 64 GB, Quad-rail NVIDIA HDR100 Infiniband, Atos EuroHPC/CINECA Italy	1,463,616	174.70	255.75	5,610
		<b><u>GPU NVIDIA Ampere</u></b>			
5	<b>Summit</b> - IBM Power System AC922, IBM POWER9 22C 3.07GHz, NVIDIA Volta GV100, Dual-rail Mellanox EDR Infiniband, IBM DOE/SC/Oak Ridge National Laboratory United States	2,414,592	148.60	200.79	10,096
		<b><u>GPU NVIDIA Volta</u></b>			





**USA**

6	<b>Sierra</b> - IBM Power System AC922, IBM POWER9 22C 3.1GHz, NVIDIA Volta GV100, Dual-rail Mellanox EDR Infiniband, IBM / NVIDIA / Mellanox DOE/NNSA/LLNL United States	1,572,480	94.64	125.71	7,438
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**GPU NVIDIA Volta**



**China**

7	<b>Sunway TaihuLight</b> - Sunway MPP, Sunway SW26010 260C 1.45GHz, Sunway, NRPC National Supercomputing Center in Wuxi China	10,649,600	93.01	125.44	15,371
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**Chinese Matrix-2000 Processor**



**USA**

8	<b>Perlmutter</b> - HPE Cray EX235n, AMD EPYC 7763 64C 2.45GHz, NVIDIA A100 SXM4 40 GB, Slingshot-10, HPE DOE/SC/LBNL/NERSC United States	761,856	70.87	93.75	2,589
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**GPU NVIDIA Ampere**



**USA**

9	<b>Selene</b> - NVIDIA DGX A100, AMD EPYC 7742 64C 2.25GHz, NVIDIA A100, Mellanox HDR Infiniband, Nvidia NVIDIA Corporation United States	555,520	63.46	79.22	2,646
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**GPU NVIDIA Ampere**



**China**

10	<b>Tianhe-2A</b> - TH-IVB-FEP Cluster, Intel Xeon E5-2692v2 12C 2.2GHz, TH Express-2, Matrix-2000, NUDT National Super Computer Center in Guangzhou China	4,981,760	61.44	100.68	18,482
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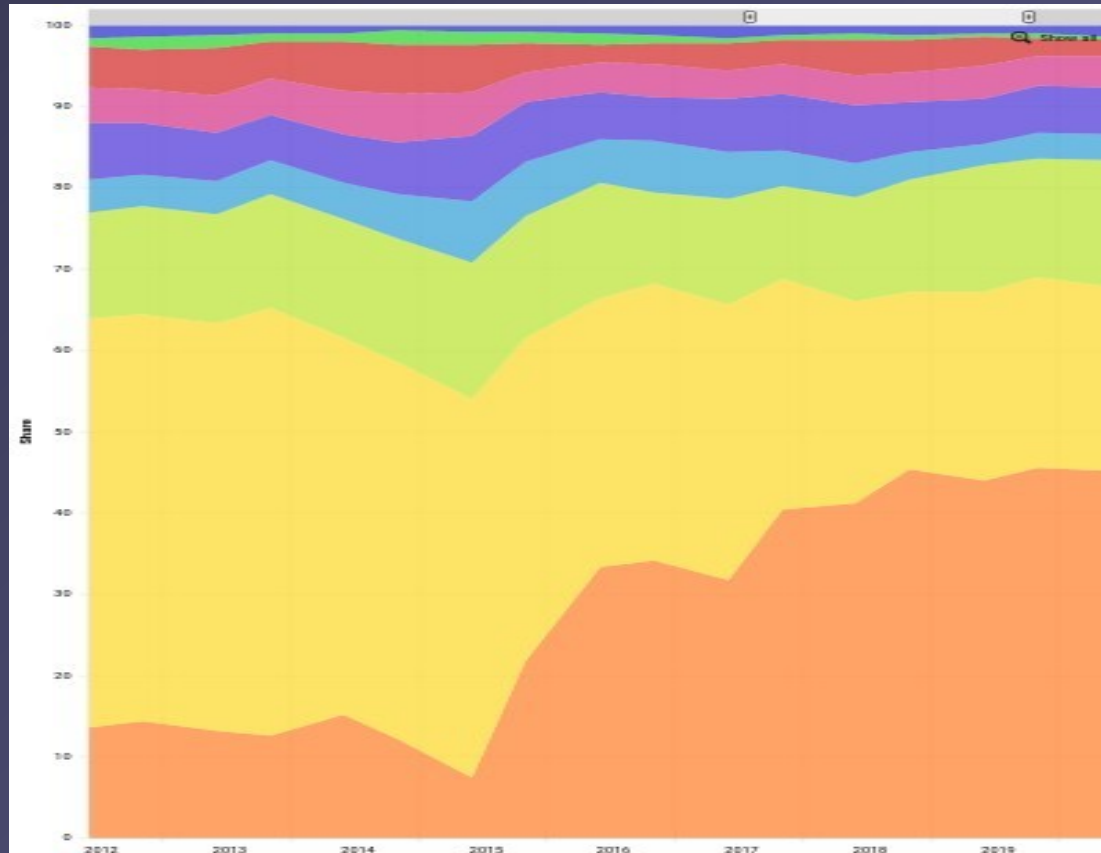
*was: Intel Xeon Phi*  
**Chinese Matrix-2000 Processor**



**北京大學**  
PEKING UNIVERSITY

# Top 500 List November 2021 – Performance Share of Countries

From <https://www.top500.org>



# LUMI

## Supercomputer, Kajaani, Finland

Using only  
Hydroelectric  
Power and its  
Heat used for  
heating buildings.

No. 3 in top500  
No. 7 in green500

2.2 million cores  
10.000 AMD GPUs



EuroHPC and LUMI consortium:  
Finland, Belgium, Czech Republic, Denmark, Estonia,  
Iceland, Norway, Poland, Sweden, and Switzerland.



RIKEN, Kobe, JAPAN

# FUGAKU



*Nature's Secrets*

# 富岳

Mt. Fuji

## The world's fastest Super Computer 2020 /2021

7.6 million cores, 442 Pflop/s

source :nytimes



Fugaku extends its reign as champion of supercomputers

JUWELS Booster 936 nodes (AMD CPU, 4x Ampere GPU)  
~450.000 AMD cores, 25 million NVIDIA Ampere GPU cores  
~ 70 Pflop/s SP ~ 44 Pflop/s DP  
No. 12 in top500 list, No. 25 in green500 list

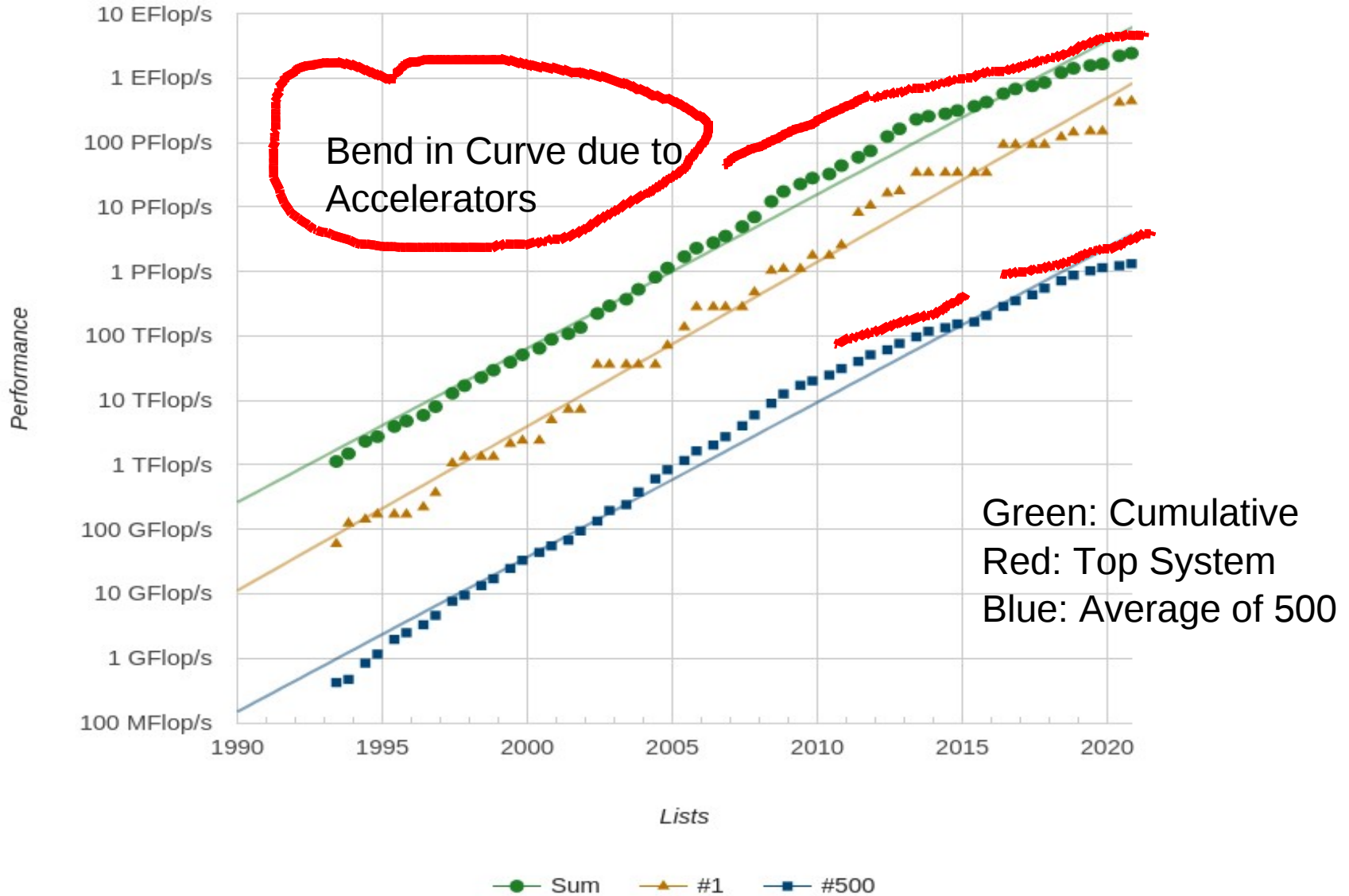
Jülich Wizard for European Leadership Science



From <https://www.top500.org>

Moore's Law?

### Projected Performance Development





# GREEN 500 list Nov. 2022

Power Efficiency  
(Gflops/Watts),

see also top500 webpage

right: 1-5

below: 6-10

Rank	Rank	System	Cores	Rmax (PFlop/s)	Power (kW)	Energy Efficiency (GFlops/watts)
6	1	Frontier - HPE Cray EX235a, AMD Optimized 3rd Generation EPYC 64C 2GHz, AMD Instinct MI250X, Slingshot-11, HPE DOE/SC/Oak Ridge National Laboratory United States	8,730,112	1,102.00	21,100	52.227
7	3	LUMI - HPE Cray EX235a, AMD Optimized 3rd Generation EPYC 64C 2GHz, AMD Instinct MI250X, Slingshot-11, HPE EuroHPC/CSC Finland	2,220,288	309.10	6,016	51.382
8	159	ATOS THX.A.B - BullSequana XH2000, Xeon Platinum 8358 32C 2.6GHz, NVIDIA A100 SXM4 64 GB, Quad-rail NVIDIA HDR100 Infiniband, Atos France	25,056	3.50	86	41.411
9	359	MN-3 - MN-Core Server, Xeon Platinum 8260M 24C 2.4GHz, Preferred Networks MN-Core, MN-Core DirectConnect, Preferred Networks Preferred Networks Japan	1,664	2.18	53	40.901
10	331	Championillon - Apollo 6500, AMD EPYC 7763 64C 2.45GHz, NVIDIA A100 SXM4 80 GB, Mellanox HDR Infiniband, HPE Hewlett Packard Enterprise France	19,840	2.32	60	38.555

**GPU AMD Instinct**

**GPU AMD Instinct**

**GPU NVIDIA Ampere**

**Intel Xeon Platinum**

**GPU NVIDIA Ampere**

Rank	Rank	System	Cores	Rmax (PFlop/s)	Power (kW)	Energy Efficiency (GFlops/watts)
1	405	Henri - Lenovo ThinkSystem SR670 V2, Intel Xeon Platinum 8362 2800Mhz (32C), NVIDIA H100 80GB PCIe, Infiniband HDR, Lenovo Flatiron Institute United States	5,920	2.04	31	65.091
2	32	Frontier TDS - HPE Cray EX235a, AMD Optimized 3rd Generation EPYC 64C 2GHz, AMD Instinct MI250X, Slingshot-11, HPE DOE/SC/Oak Ridge National Laboratory United States	120,832	19.20	309	62.684
11		Adastra - HPE Cray EX235a, AMD Optimized 3rd Generation EPYC 64C 2GHz, AMD Instinct MI250X, Slingshot-11, HPE Grand Equipement National de Calcul Intensif - Centre Informatique National de l'Enseignement Suprieur (GENCI-CINES) France	319,072	46.10	921	58.021
15		Setonix - GPU - HPE Cray EX235a, AMD Optimized 3rd Generation EPYC 64C 2GHz, AMD Instinct MI250X, Slingshot-11, HPE Pawsey Supercomputing Centre, Kensington, Western Australia Australia	181,248	27.16	477	56.983
68		Dardel GPU - HPE Cray EX235a, AMD Optimized 3rd Generation EPYC 64C 2GHz, AMD Instinct MI250X, Slingshot-11, HPE KTH - Royal Institute of Technology Sweden	52,864	8.26	146	56.491

**GPU NVIDIA Hopper**

**GPU AMD Instinct**

**GPU AMD Instinct**

**GPU AMD Instinct**

**GPU AMD Instinct**