

# Dynamical mechanisms of accretion in galactic nuclei.

Study of stability, accretion, and tidal disruption near supermassive black holes with advanced parallel computer technologies.





12 referred + 4 in progress (2020 – 2021): MNRAS – 7; A&A – 4; ApJ – 1 ~97 citations... Dr. Sci. Peter Berczik Dr. Alexander Veles Dr. Marina Ishchenko Margarita Sobolenko Daniel Ivanov

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### BH's in galaxies (MW - Sgr A\*)



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## **BH's in galaxies**





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#### THE $M-\sigma$ AND M-L RELATIONS IN GALACTIC BULGES, AND DETERMINATIONS OF THEIR INTRINSIC SCATTER

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#### **Galaxy Collisions**



#### **Galaxy Collisions** $\approx$ **BH's collisions**



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Multiple Massive Black Holes NGC6240 strong ongoing merger... Komossa et al. 2002

Two AGN in each of Nuclei separation ~1kpc Chandra X-Ray M82: The bright spots in the center are supernova remnants and X-ray binaries. The luminosity of the X-ray binaries suggests that most contain a black hole. A close encounter with a large galaxy, M81, in the last 100 Myr is thought to be the cause of the starburst activity. Ebisuzaki et al. 2002



#### Galaxy Collisions ≈ BH's collisions

#### **Swift-detected Active Black Holes in Merging Galaxies**



UGC 06527







NGC 7319



MCG 0212050



NGC 1142



NGC 2992

### Double & Triple SMBH evolution in galaxy centers. New hybrid code approaches and performance tests.



1:10 minor merger

Two SMBH's ~10^6 Mo

High accuracy direct summation: ~3M Particles! First time reach the < 1 mpc separation of BBH from initial ~1 kpc scale

Full up to 3.5 PN accurate BBH dynamics!



and final merger. The line-of-sight is perpendicular to the orbital plane and the maps are 1.8 kpc wide. While fairly symmetric before the pericenter (top left), the density distribution becomes clearly asymmetric after the pericenter (middle). After the final merger, a thick  $\sim 10^9 M_{\odot}$  gaseous nuclear disk is formed (bottom), in which the two SMBHs start orbiting.

Figure 1. Mass-weighted gas density maps during last pericenter



FIG. 3.— Same as in Figure 1 but at T = 40 Myr. Both SMBHs are embedded in a single cusp.







Harfst et al, NewA, <u>12</u>, 357 (2007) [astro-ph/0608125]

#### **Hierarchical Individual Block Time Steps**



ftp://ftp.mao.kiev.ua/pub/berczik/phi-GRAPE/





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i, j – particle

#### Some communication scheme...



#### http://adsabs.harvard.edu/abs/2011hpc..conf....8B

• active part. scan:	$O(N_{act} log(N_{act}))$	+ $T_{\rm host}$
• all part. prediction:	O(N/N <sub>GPU</sub> )	+ $T_{\rm host}$
• ''j'' part. send. to GPU:	O(N/N <sub>GPU</sub> )	+ $T_{\rm comm}$
• ''i'' part. send. to GPU:	$O(N_{act})$	+ $T_{\rm comm}$
• ''force'' determ. on GPU:	$O(N N_{act}/N_{GPU})$	+ $T_{\rm GPU}$
• receive the ''force'':	$O(N_{act})$	+ $T_{\rm comm}$
• MPI global comm.:	$O((\tau_{\text{lat}}+N_{\text{act}}) \log(N_{\text{GPU}}))$	+ $T_{\rm MPI}$
• corr. for ''i'' part.:	$O(N_{act})$	+ $T_{\rm host}$

ftp://ftp.mao.kiev.ua/pub/berczik/phi-GPU/

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$$\Delta T_{total} = \Delta T_{host} + \Delta T_{GPU} + \Delta T_{comm} + \Delta T_{MPI}$$

$$\Delta T_{MPI} \propto (\tau_{lat} + N_{act}) \cdot \log(N_{GPU})$$

$$\Delta T_{GPU} \propto N \cdot \frac{N_{act}}{N_{GPU}}$$

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#### **Initial Condition**



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Code development together with Yohai ...



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## **Our CPU/GPU "hybrid" N-body code**



## **Double & Triple SMBH evolution in galaxy centers. New hybrid code approaches and performance tests.**

#### MAO 3+2 new nodes 4xGF 2080S + 4xGF 3070



4 x GF 2080S, 3072 SP @ 1.81 GHz 4 x GF 3070, 5888 SP @ 1.77 GHz JUWELS Booster consists of 936 compute nodes, each equipped with 4 NVIDIA A100 GPUs. The GPUs are hosted by AMD EPYC Rome CPUs. The compute nodes are connected with HDR-200 InfiniBand in a DragonFly+ topology.



The configuration of JUWELS Booster compute nodes is the following

- CPU: AMD EPYC 7402 processor; 2 sockets, 24 cores per socket, SMT-2 (total: 2×24×2 = 96 threads) in NPS-4 [1] configuration (details on WikiChip)
- **Memory**: 512 GB DDR4-3200 RAM (of which at least 20 GB is taken by the system software stack, including the file system); 256 GB per socket; 8 memory channels per socket (2 channels per NUMA domain)
- GPU: 4 × NVIDIA A100 Tensor Core GPU with 40 GB; connected via NVLink3 to each other
- Network: 4 × Mellanox HDR200 InfiniBand ConnectX 6 (200 Gbit/s each), HCA
- **Periphery**: CPU, GPU, and network adapter are connected via 2 PCIe Gen 4 switches with 16 PCIe lanes going to each device (CPU socket: 2×16 lanes). PCIe switches are configured in *synthetic mode*.



The InfiniBand network of JUWELS Booster is implemented as a DragonFly+ network.

48 nodes are combined in a switch group (*cell*), interconnected in a full fat-tree topology, with 10 leaf switches and 10 spine switches in a two-level configuration. 40 Tbit/s of bi-section bandwidth is available.



Sketch of the network topology within a JUWELS Booster cell with 48 nodes (N1 to N 48), 10 level 1 switches (L1 1 to L1 10) and 10 level 2 switches (L2 1 to L2 10). Only a small subset of the total amount of links are shown for readability. The purple, 20th link leaving each level 2 switch should indicate the connection to JUWELS Cluster, while the other 19 outgoing level 2 links connect to other cells.

#### ACCELERATING HPC





















#### **Our** φGRAPE/GPU (parallel) N-body code



Number of MPI processes (GPU's): -  $N_{p}$ 

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 $\phi$ GPU (@ JUWELS A100). Plummer: G=M=1; E<sub>TOT</sub>=-1/4;  $\epsilon$ =10<sup>-4</sup>; t<sub>end</sub>=1 10.0 [hour] 1.0  $\mathbf{T}_{\texttt{tot}}$ 0.1 time running 0.0 Total 0.0 -128K 256K 512K 1 M 2M 0.0 8 16 32 64 4 Number of MPI processes (GPU's):  $-N_{p}$ 







